

EVK ATOP

REFERENCE DESIGN KIT



Product Description

The ATOP Reference Design Kit provides an environment to showcase all features of ATOP 2.5G and ATOP 3.5G modules, allowing customers to quickly build product demonstrators without having to develop their own hardware first. This development tool is supplied solely for the purpose of testing. The Reference Design Kit is available for both ATOP 2.5G and 3.5G modules.

The kit includes

- A main board containing the ATOP module and most final application interfaces
- A debug board with additional interfaces primarily for development only
- Several accessories: Cellular antenna, GPS/GLONASS antenna, NFC antenna, LCD Display, Li-Polymer backup battery, backup coin cell

Key Benefits

The ATOP Reference Design Kit is a tool designed for engineers, programmers and developers who intend to:

- Develop and test applications based on current and future modules of the ATOP Series.
- Program or update an ATOP module
- Debug or modify applications based on ATOP modules
- Implement full applications without the need for an external microprocessor
- Develop simpler first-pass proof-of-concept devices for a new application

The robust future-proof environment significantly reduces time-to-market

AVAILABLE FOR

[EMEA](#)

[North America](#)

[Latin America](#)

[Japan](#)

[Korea](#)

[Australia](#)

EVK ATOP

Product Features

- The kit includes a main board, a debug board with additional I/O options and several accessories.
- Program and update any ATOP module
- Create and debug applications based on ATOP modules
- Develop simpler first-pass proof-of-concept prototypes for a new application
- Implement full applications without the need for an external microprocessor

Main Board Interfaces

- USB HS interface to the ATOP Application processor and USB FS interface to the ATOP Interface processor
- CAN bus interface
- Ethernet RJ45 interface
- Cellular antenna connectors (2 SMA switchable)
- GNSS antenna connectors (2 SMA switchable between active & passive + 1 UFL passive only)
- NFC antenna connector
- RESET and Power ON button
- Accelerometer
- Programmable LEDs
- SIM card holder
- SDIO card holder for the ATOP application CPU
- Analog audio connector(for headset – speakers and microphone)
- Battery connector
- Coin cell holder for RTC support
- Board-to-board connector for the debug board

Debug Board Interfaces

- LCD display
- SDIO card holder for the ATOP utility processor
- JTAG connectors for debugging
- Additional USB connector to the ATOP interface processor
- Additional reset buttons for use during debugging & SW updating
- Programmable push buttons(e.g. eCall)
- Board-to-board connector for the main board

Software and Tools

- All ATOP development support tools are available for MS Windows© host. Alternatively Linux may be used as host.
- Eclipse IDE for JAVA development on Application CPU
- IDE for Utility CPU development in 'C' etc.
- USB and UART tools
- Memory Flashing tools
- Modem logging tools (MS Windows© only)
- Training tools with sample code e.g. Telephony, Audio, Location, Mobile IP Data, Cell Network Management, NFC, FOTA, debugging etc.
- Modem baseband software
- Access to online issue tracking, FAQs, tech support as well as engineering support team.

Electrical & Sensitivity

- The power supply input to the EVK is the USB port for the Interface Processor. Use a 5V Power supply (1A recommended)



Join the Telit Technical Forum

For a quicker and more rewarding integration experience join the Telit Technical Forum. There you can browse the first open forum covering all IoT topics, get direct support by region (EMEA, North America, Latin America, APAC), take part in this quickly growing IoT community and exchange experiences.