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**Title** L506 Reference Design

Sheet \_\_\_\_\_ of \_\_\_\_\_

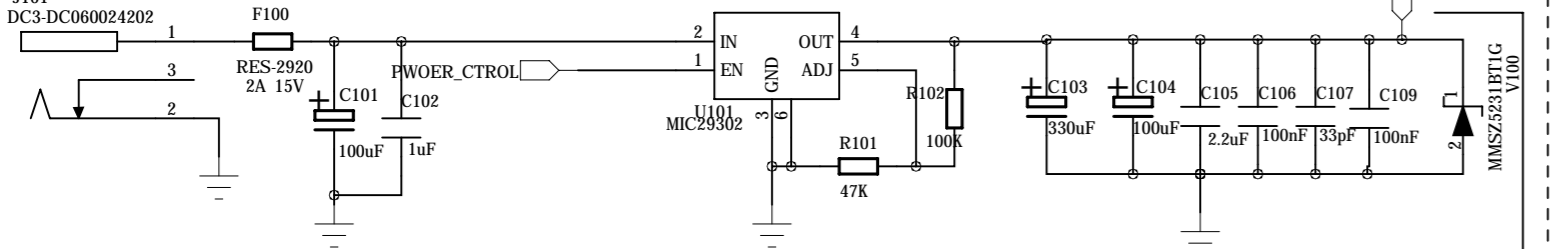
Sub Name \_\_\_\_\_ Rev \_\_\_\_\_

Date: \_\_\_\_\_ Sheet \_\_\_\_\_ of \_\_\_\_\_

# POWER SUPPLY

**Note:**  
The voltage converter should provide current at least 2.0A.  
It is used when the DC input voltage is below 7V.

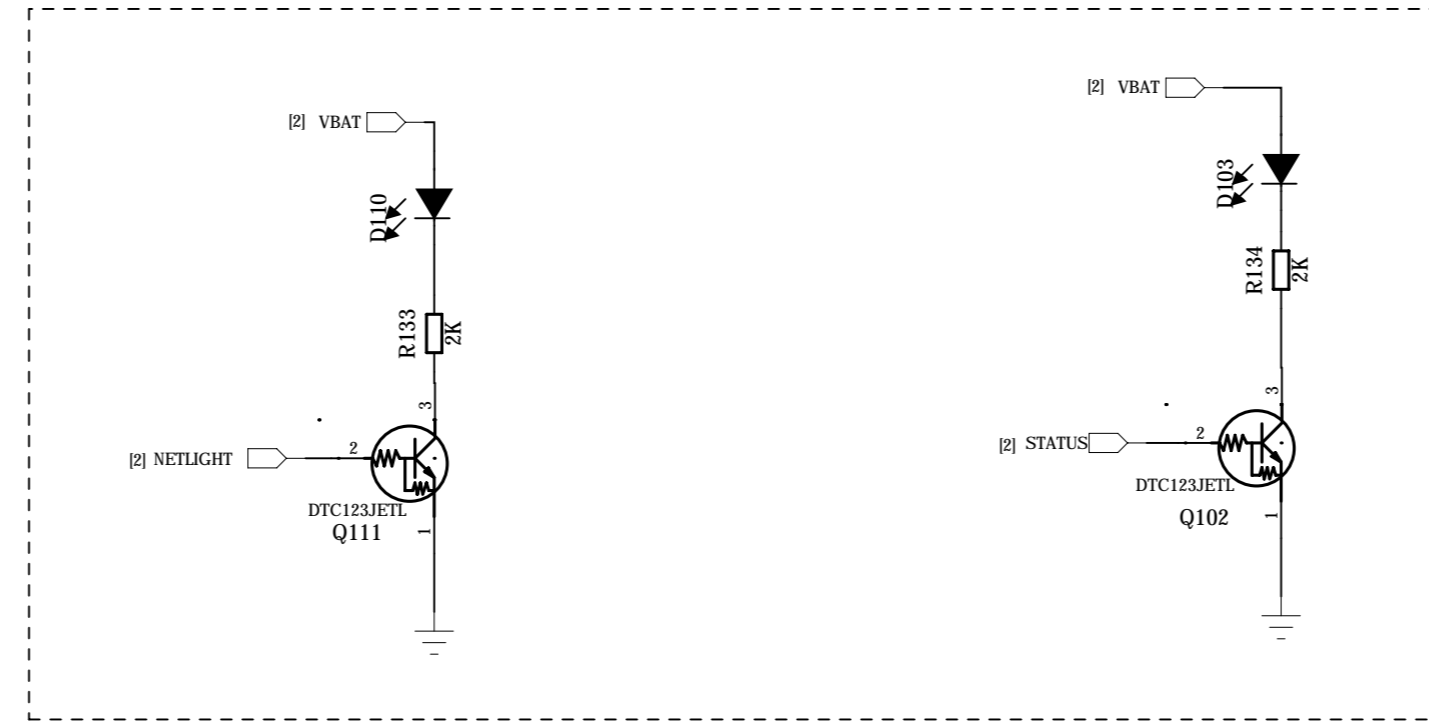
## DC\_5V\_IN



The designed output for the power supply is 3.8V.

- VBAT ranges from 3.4V to 4.2V we strongly suggest use 3.8V.
- Module drains the maximum current around 2.0A in burst time (577us). It's will cause the Voltage drop about 350mV.
- The width of VBAT trace is recommended to be more than 2mm.
- Capacitance is arranged in ascending order, the smallest one closes to the VBAT pad, and keep all capacitance as close to the VBAT pad as possible.
- Strongly recommended on the VBAT feet parallel a 5.1V / 500mw Zener diode. To prevent module caused by surge voltage. The zener diode will placed near the VBAT feet.

# LED

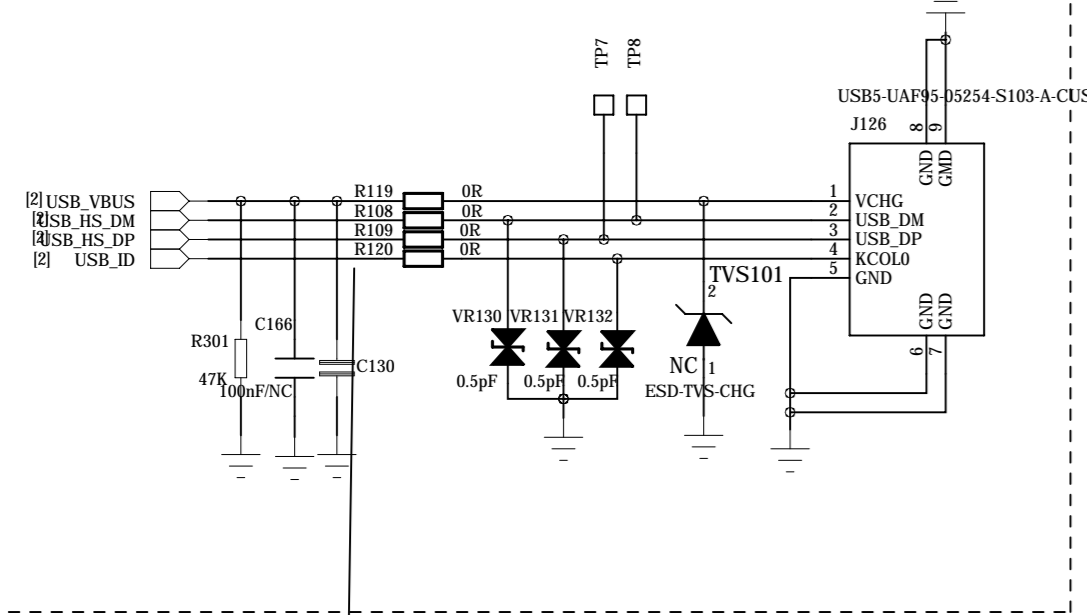


# L506 MODULE

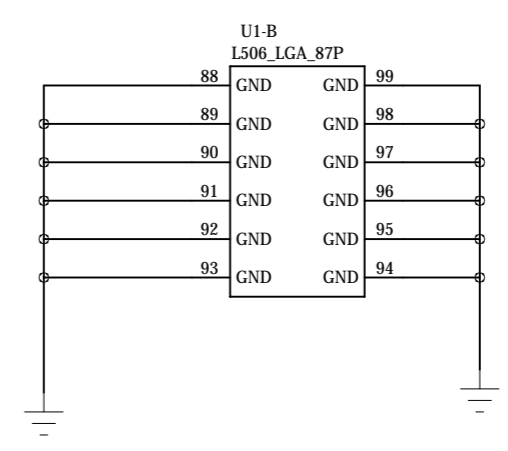
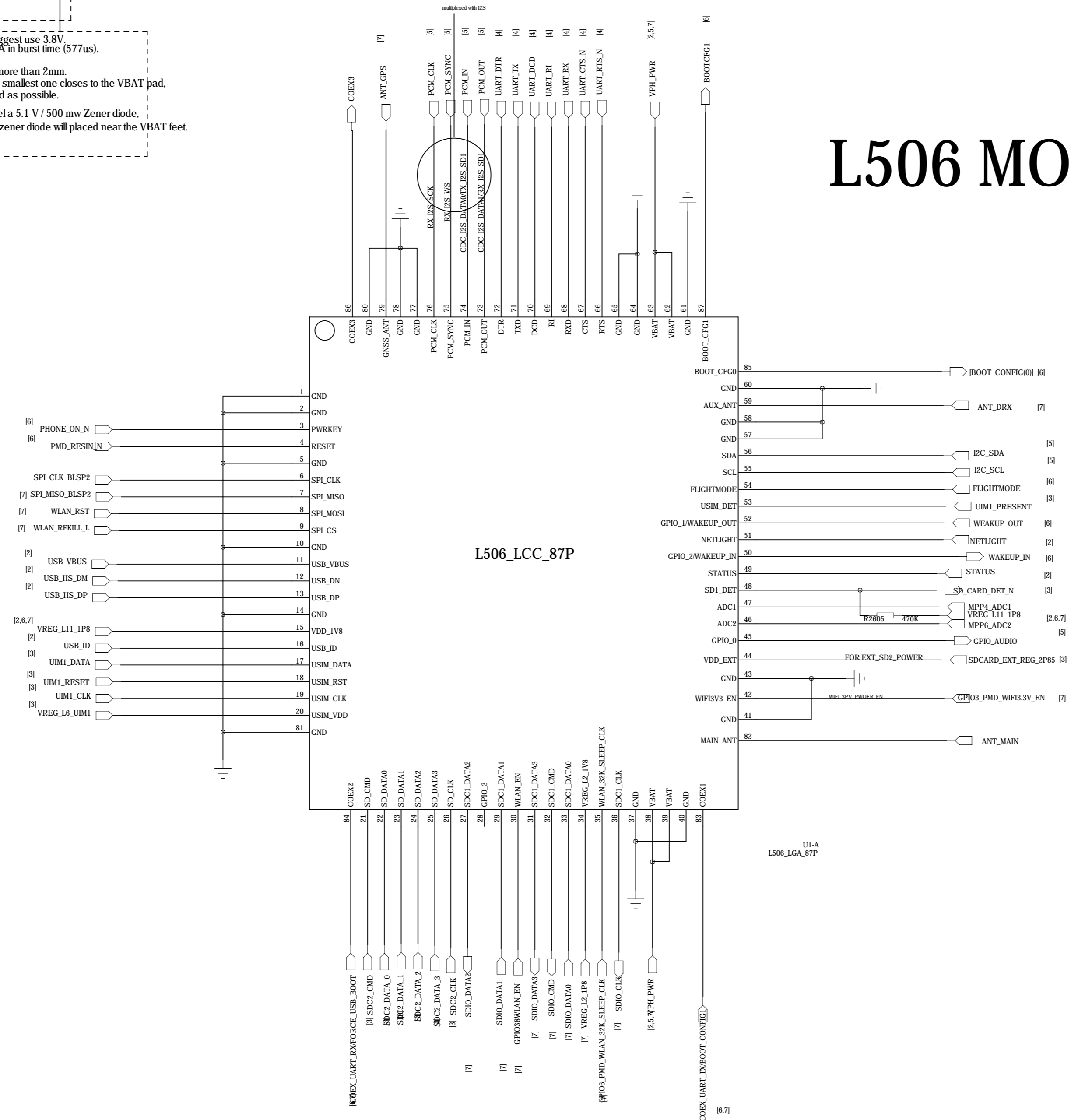
L506 module have pulled up to 1.8V internal

# USB\_CONNECTOR

NOT Support usb charge

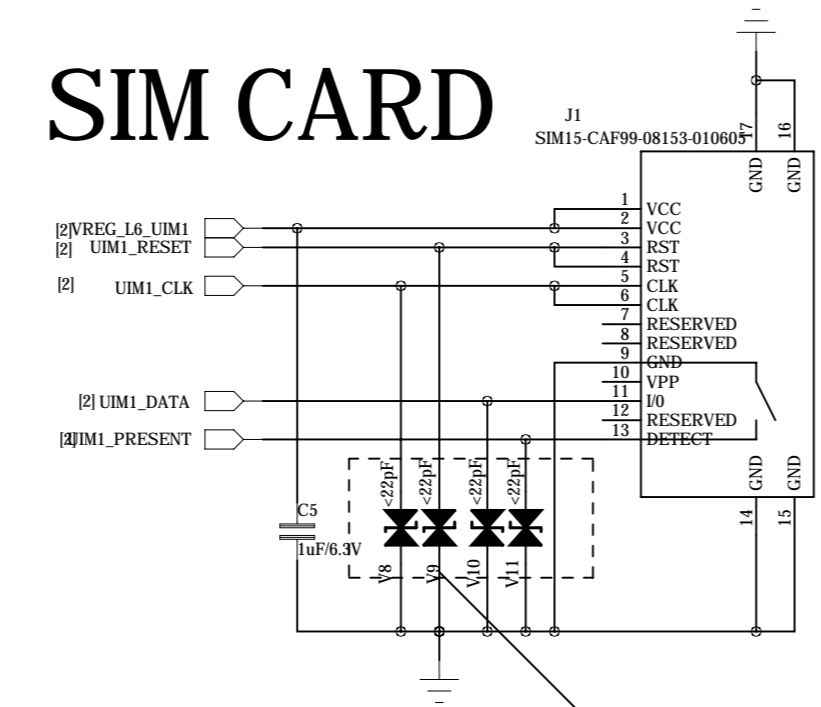


- VR130/VR131 must be less than 2pF about junction capacitance
- L506 Support USB 2.0 High Speed, the USB\_DM and USB\_DP must route 90 ohm differential
- Recommended that the rated voltage of the capacitance on the Vbus net is 25V.



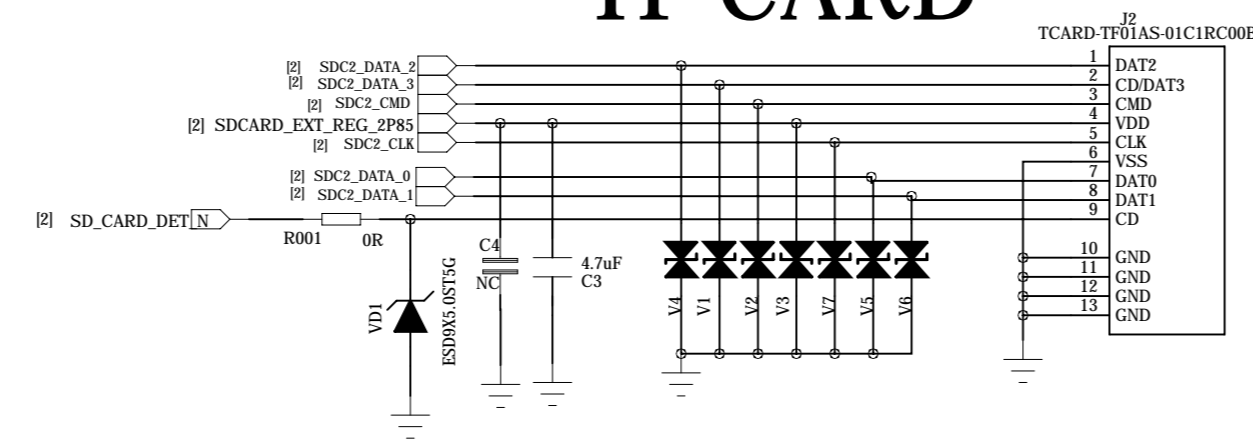
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# SIM CARD

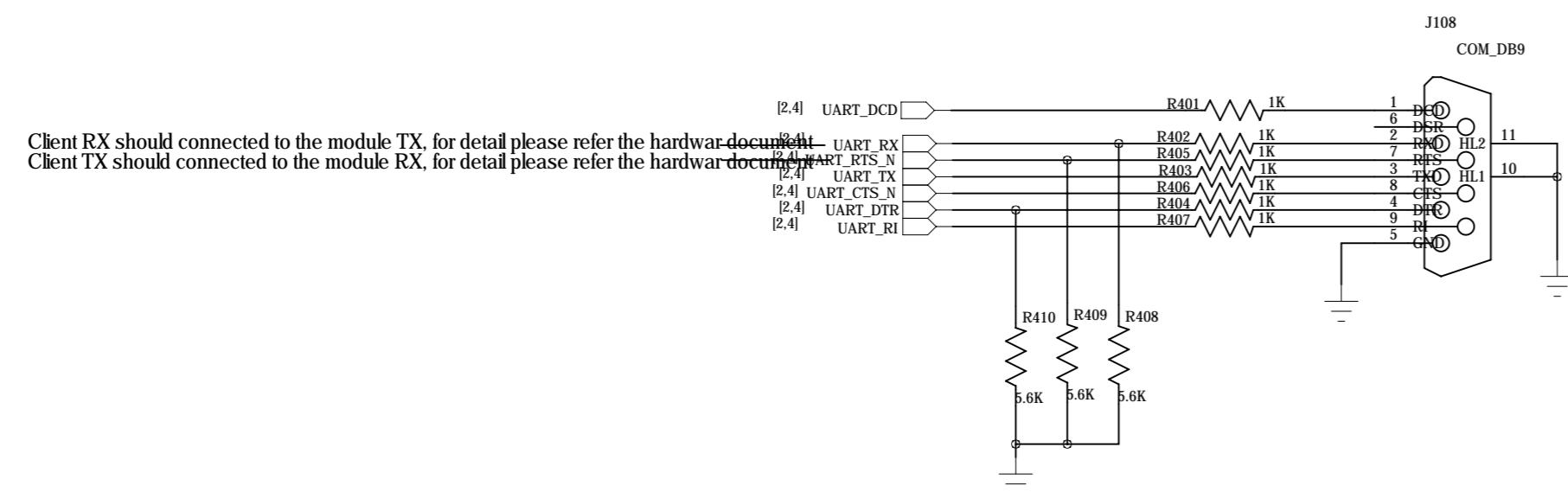


The ESD should placed close to the SIM Socket and ESD typical capacitance must below 30pF

# TF CARD



# UART PORT

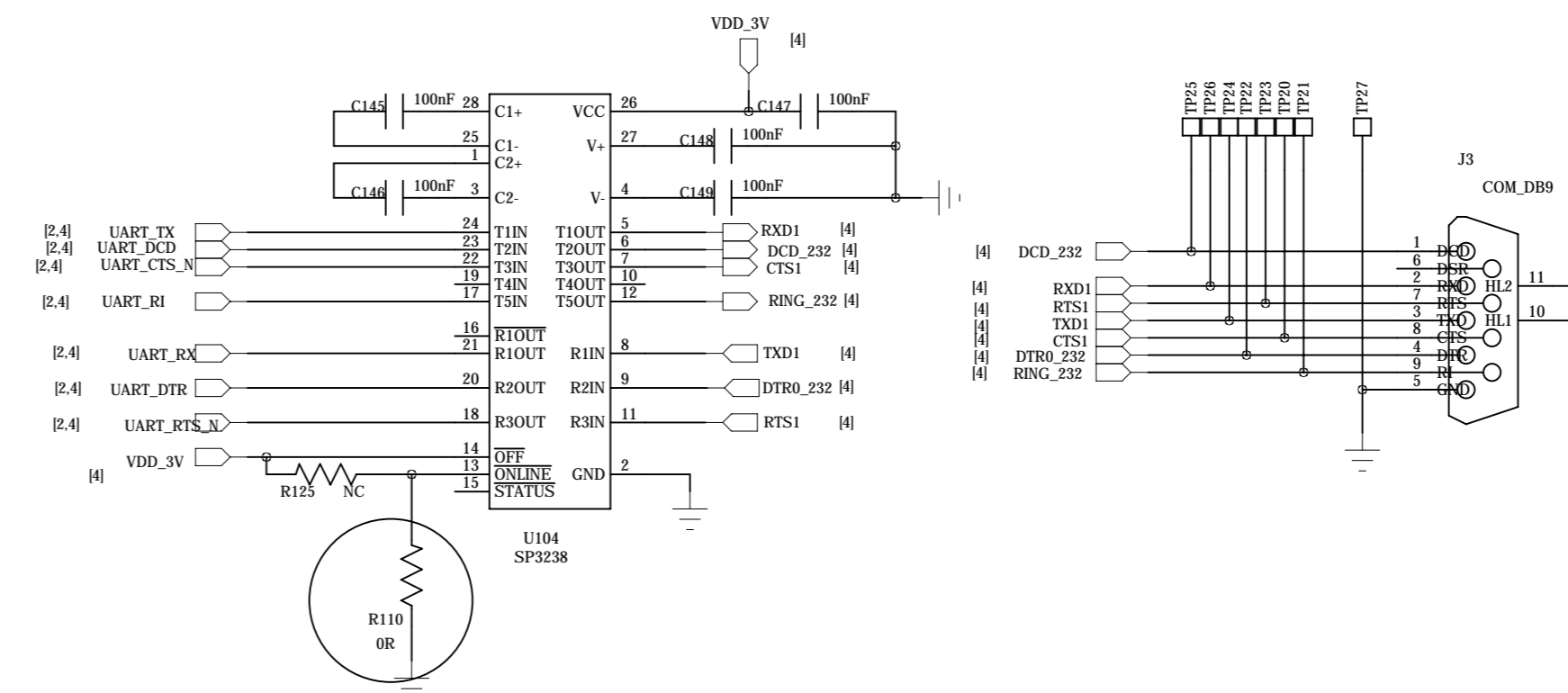


Client RX should connected to the module TX, for detail please refer the hardware document.  
Client TX should connected to the module RX, for detail please refer the hardware document.

Connection of All Functional UART Port for 1.8V System

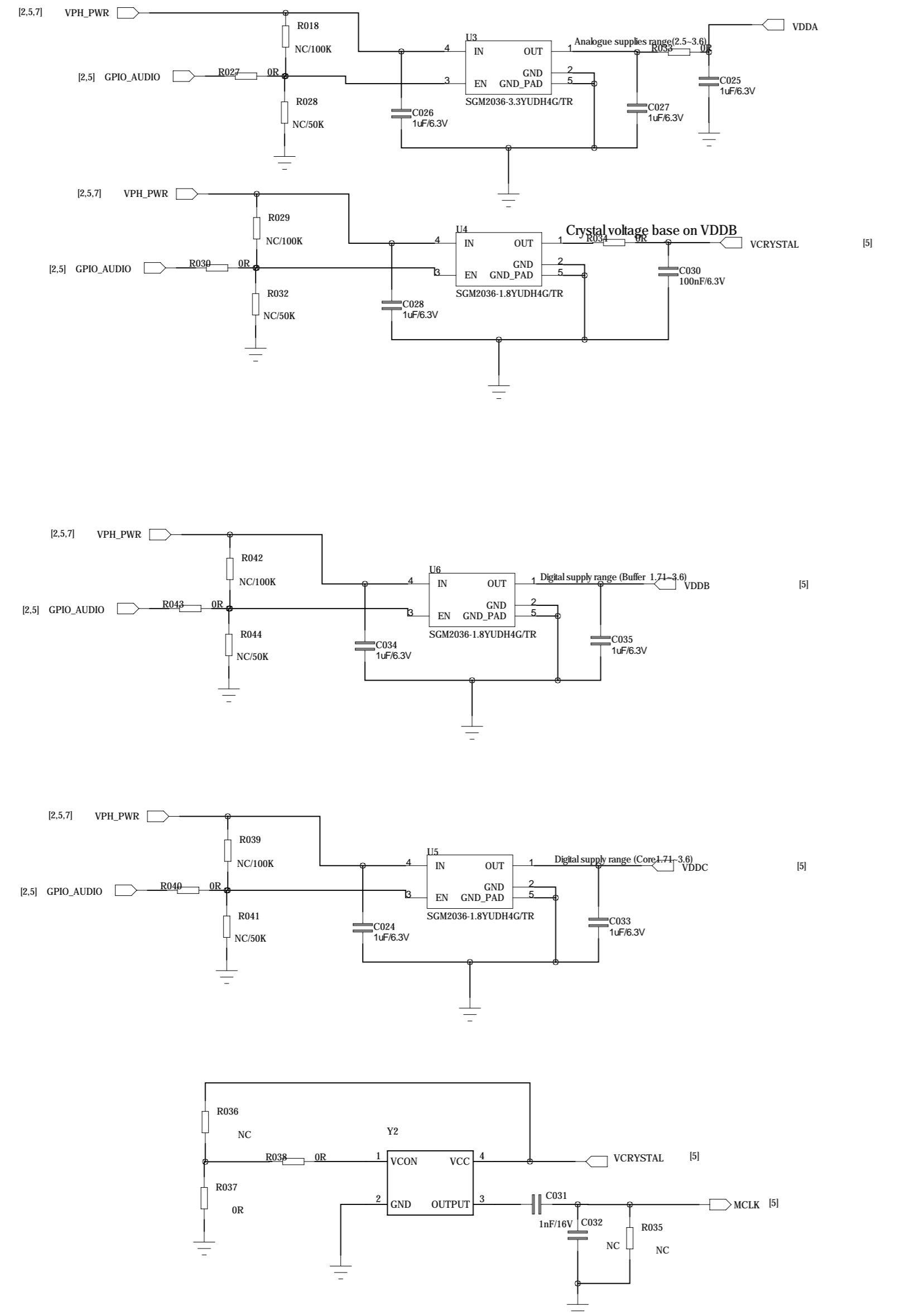
**Note**

1. CTS&RTS will be used for HW flow control when mass data has been sent.
2. When AT+QSCLK=1 is set on the module, customer's application can control
3. When DTR is set to high level, and there is no on-air or hardware interrupt, such as GPIO interrupt or data on serial port, the module will enter into sleep mode automatically.
4. RI will output an indication signal when activity such as voice call or SMS is coming.
5. DCD is mainly applied in modem communication (PPP), the active status represents the communication link has been set up.
6. Please pay attention to the level match of UART in product application.
7. The L506 UART is 1.8V interface. A level shifter should be used if user's application is equipped with a 3.3V UART interface.

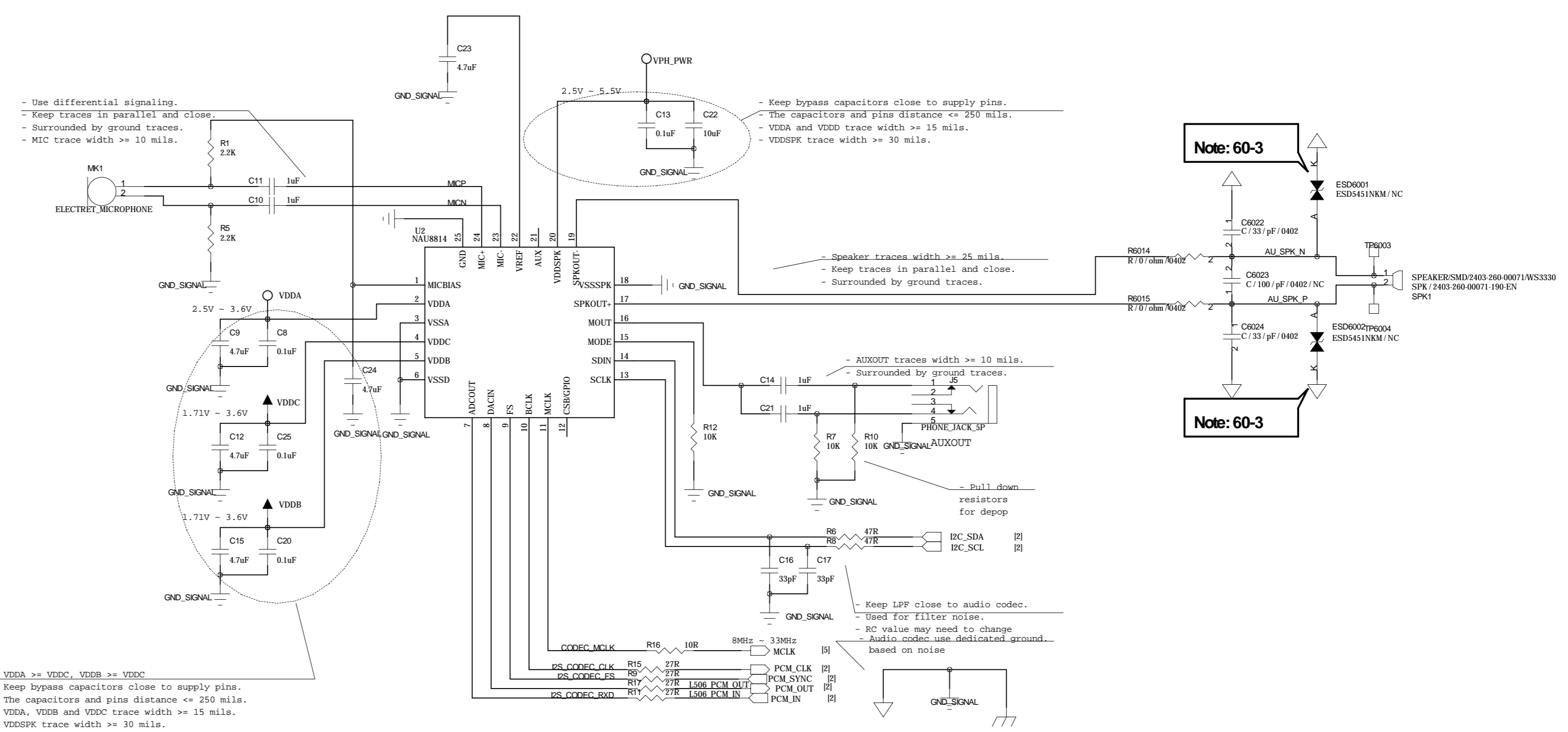


To comply with RS-232-C protocol, the RS-232-C level shifter chip should be used to connect L506 to the RS-232-C interface.  
In this connection, the TTL level and RS-232-C level are converted mutually.  
we recommends that user uses the SP3238ECA chip with a full modem.  
For more information please refers to the RS-232-C chip datasheet.

# PCM PORT (OPTIONAL)



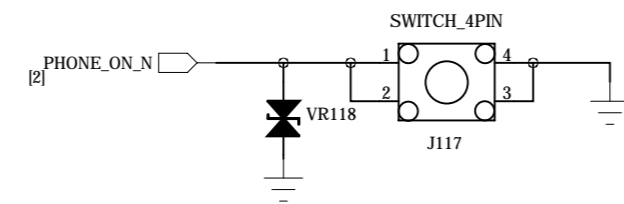
Note 60-3: ESD diodes are for enhancement proposal; also could be cost reduction.



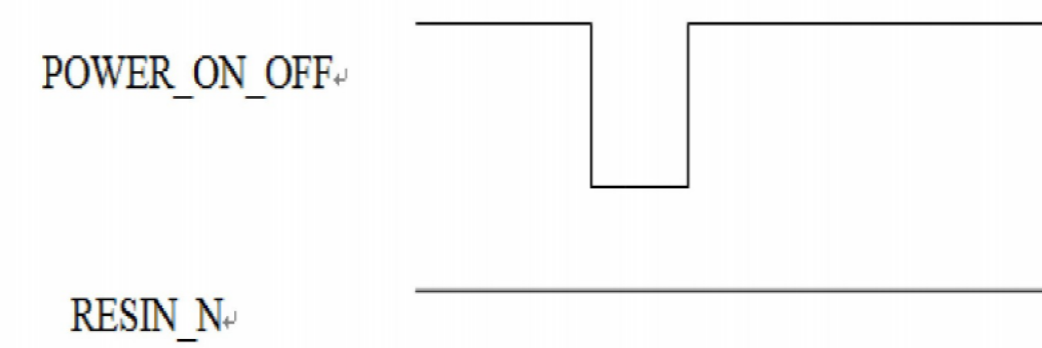
- VDDA >= VDDC, VDDB >= VSSD  
 - Keep bypass capacitors close to supply pins.  
 - The capacitors and pins distance <= 250 mils.  
 - VDDA, VDDB and VDDC trace width >= 15 mils.  
 - VDDSPK trace width >= 30 mils.

1. PCM design is a optional reference for user. In our reference design used the NAU8814 as the codec chip, the user also can choose the other codec chip as the audio processor.
2. L506 support A full function PCM port. (NAU8814 only support A MIC in and A Speaker out) the audio function depend on codec chip
3. Audio system is a very complexity system. It is not only related to circuit also has to do with sound cavity structure

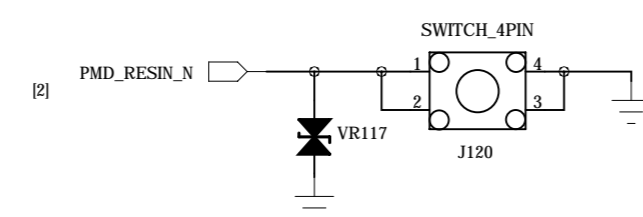
# POWER KEY



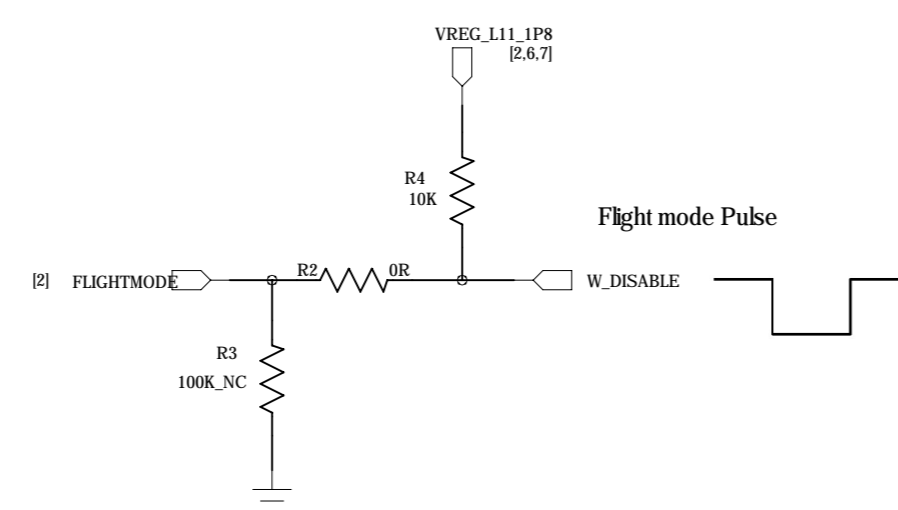
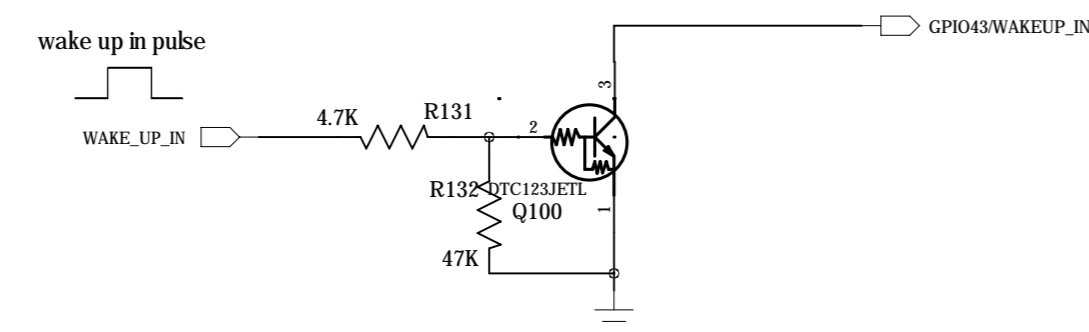
**Note#**  
The module power on sequence must after the MCU and the power on diagram as below timing chart  
As shown in figure, Application processors need to control the formation of POWER\_ON\_OFF pulse, the pulse width should be in 8 seconds or so.  
If the PPP connection has been established, please disconnect



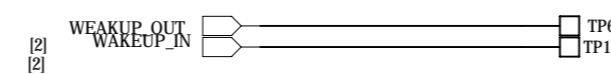
# RESET KEY



**Note#**  
Reset active low input must keep above 105ms



# Interactive control

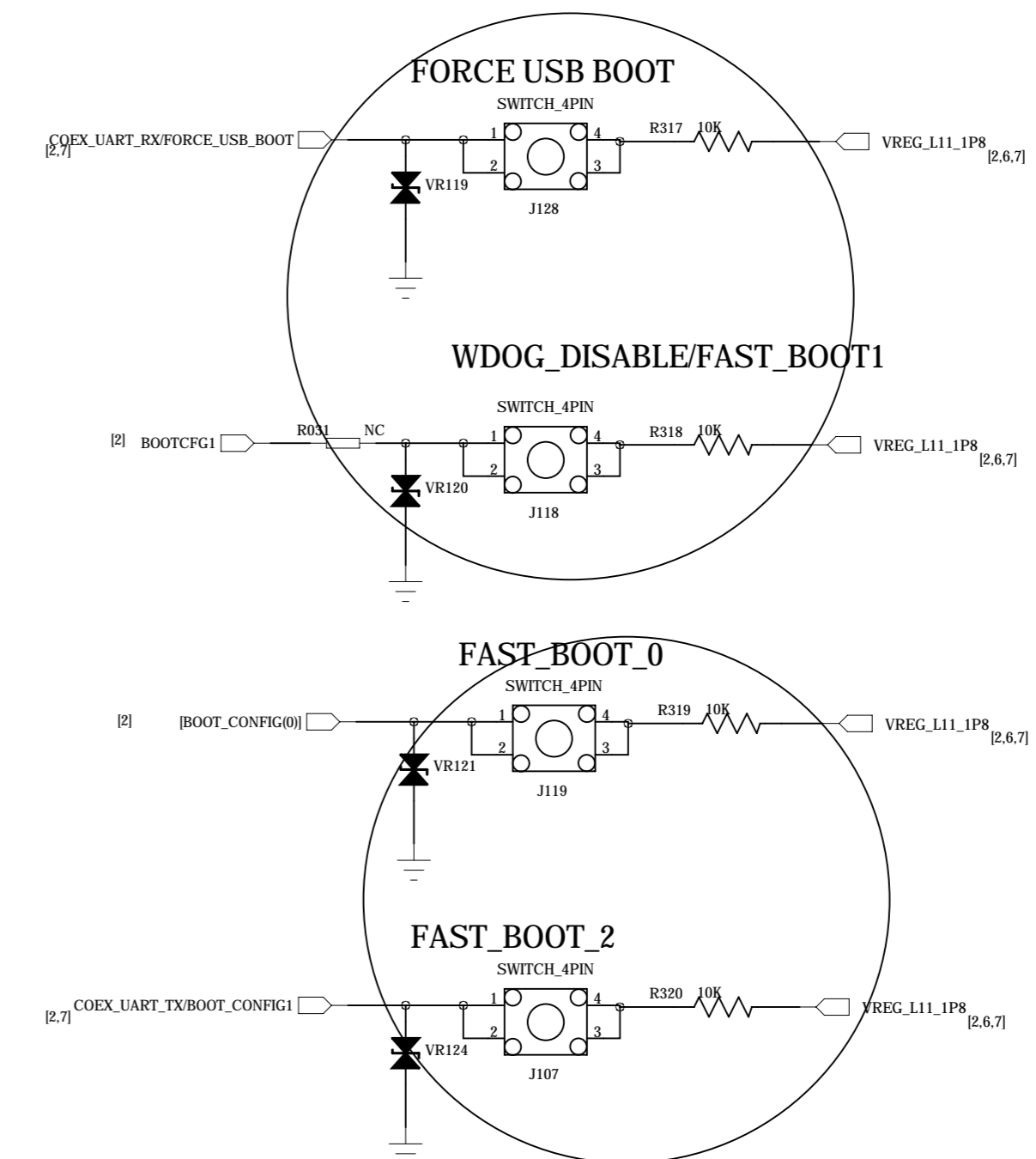


L506 provides three shook hands with application processor communication signals.  
Application processor can query whether the module boot normal work through MDM\_POWERON.  
Through the MDM\_READY query module is in sleep mode, and sleep in the module, through AP\_WAKEUP\_MDM wake module.  
Similarly, when application processor in the sleep state, the L506 modules can through MDM\_WAKEUP\_AP wake application processor.  
WAKEUP\_IN# The host can lower the signal awakens the moduleHL, low level has maintained, module can't sleep.  
WAKEUP\_OUT# When L506 need to communicate with the AP, module can be set this pin for low level to awaken application processor.  
FLIGHTMODE# Through the external output high level module into flight mode;  
STATUS# Module sleep instructions, high level indicator to sleep, low level instructions for the awakened state;

BOOT CONFIGURATION TABLE

| BOOT_CONFIG[3:1] | BOOT OPTIONS |
|------------------|--------------|
| 0b000            | NAND -> USB  |
| 0b001            | Only USB     |

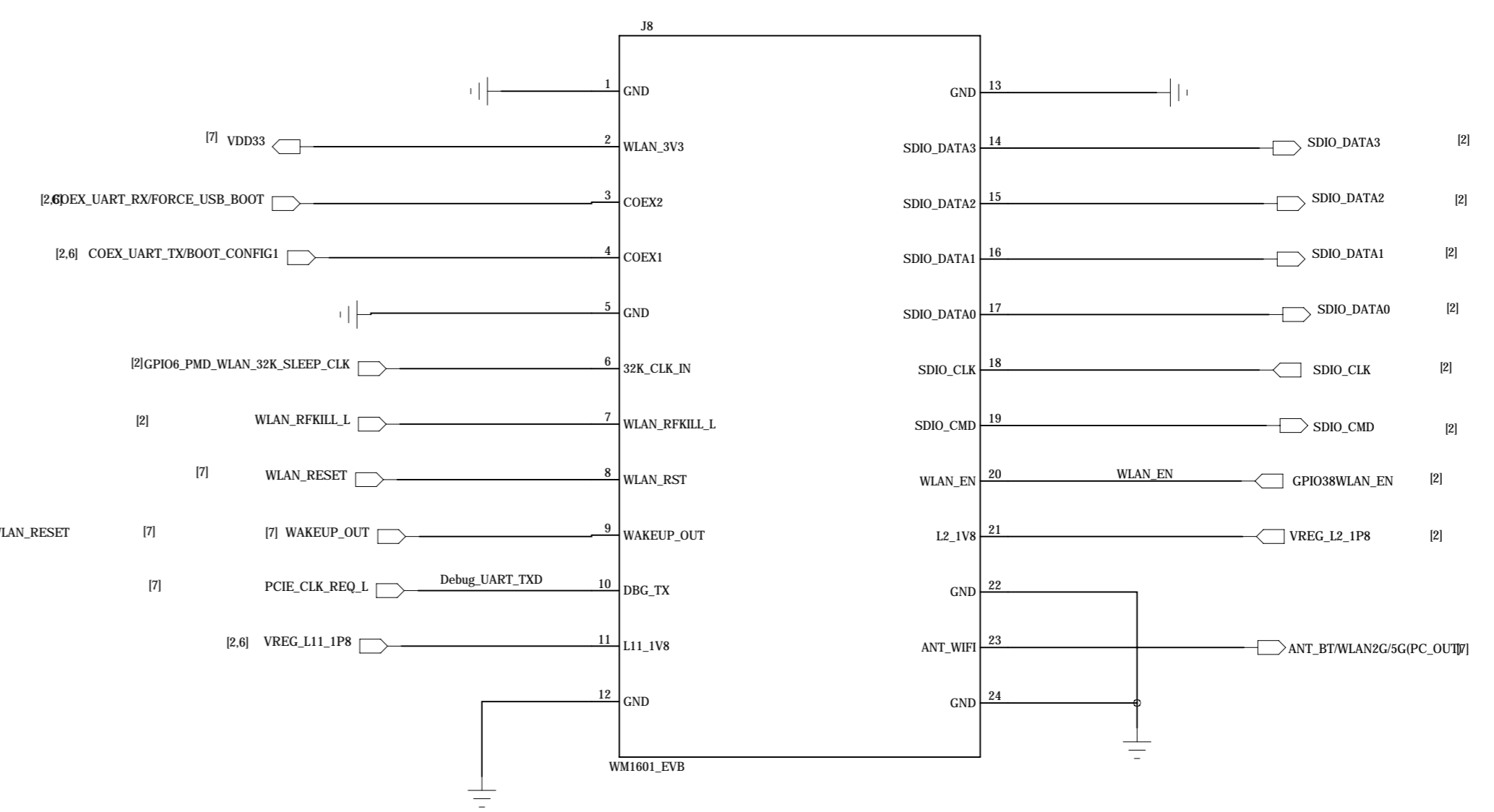
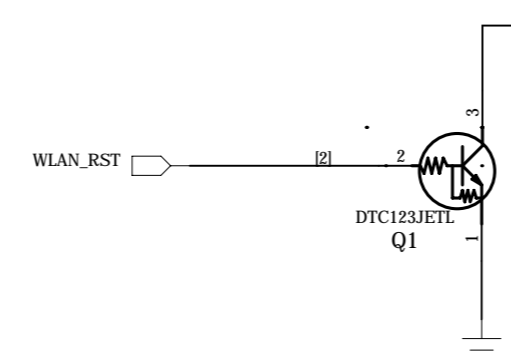
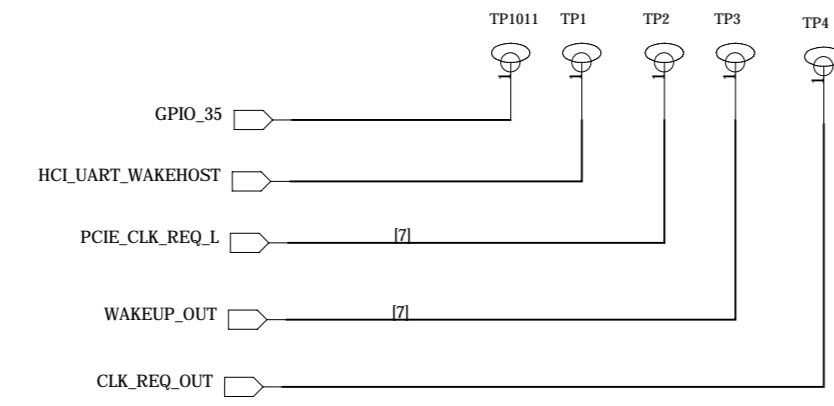
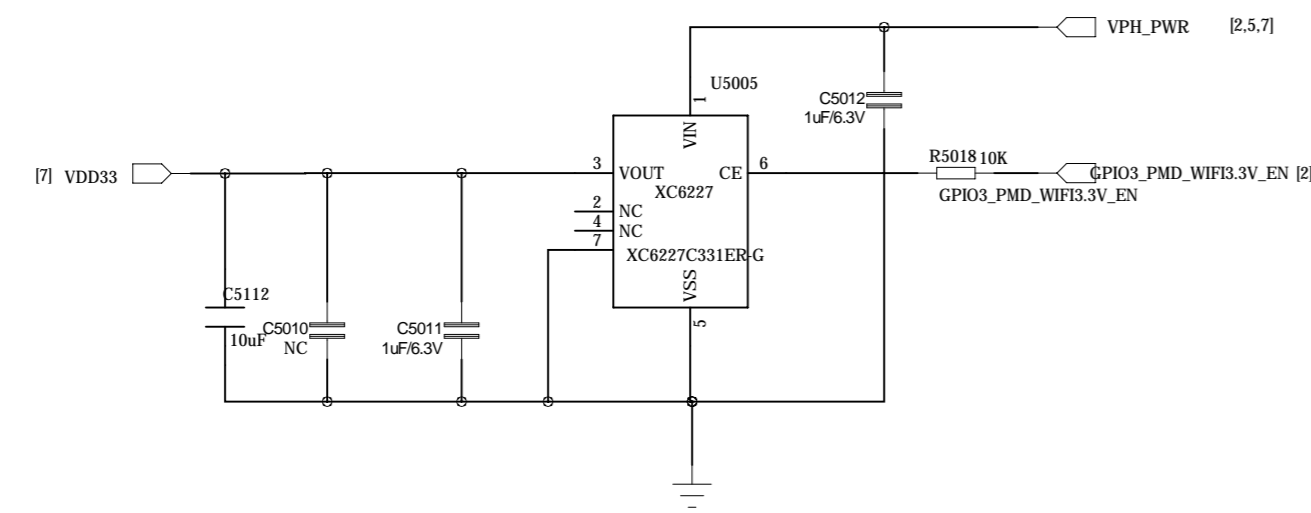
Note: Default boot config (0b000) is NAND



**Note#**  
Pull up this net(COEX\_UART\_RX/FORCE\_USB\_BOOT) to 1.8V force the module in force usb boot mode.

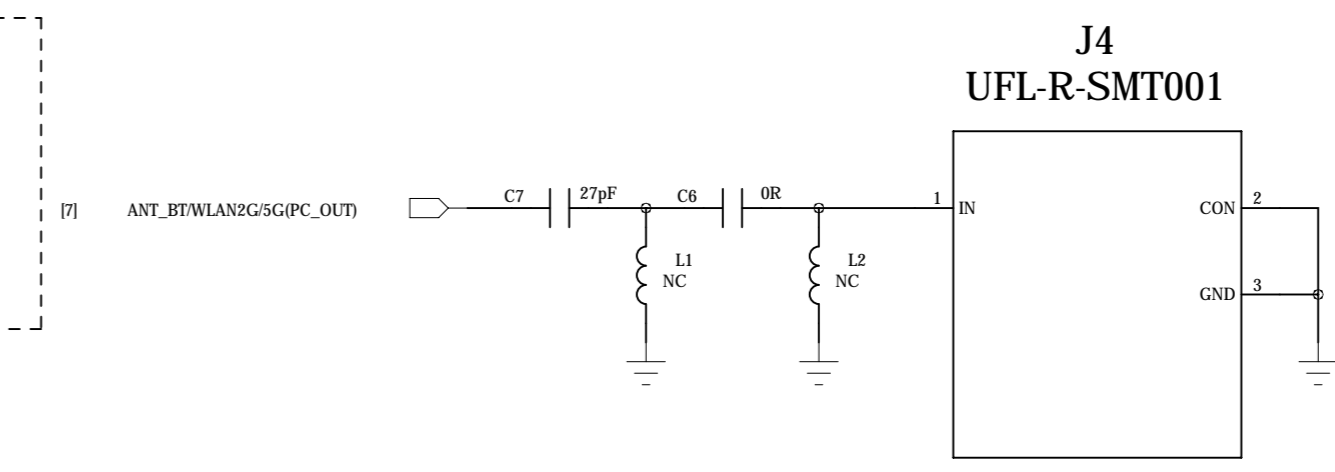
# WiFi Module(WM1601)

## WiFi 3.3V



**Note:**  
1. WiFi design is an optional reference for user. In our reference design used the WM1601 as the WiFi solution. for more information about WM1601 please contact mobiletek market

## WiFi Connector



## Antenna Connector

